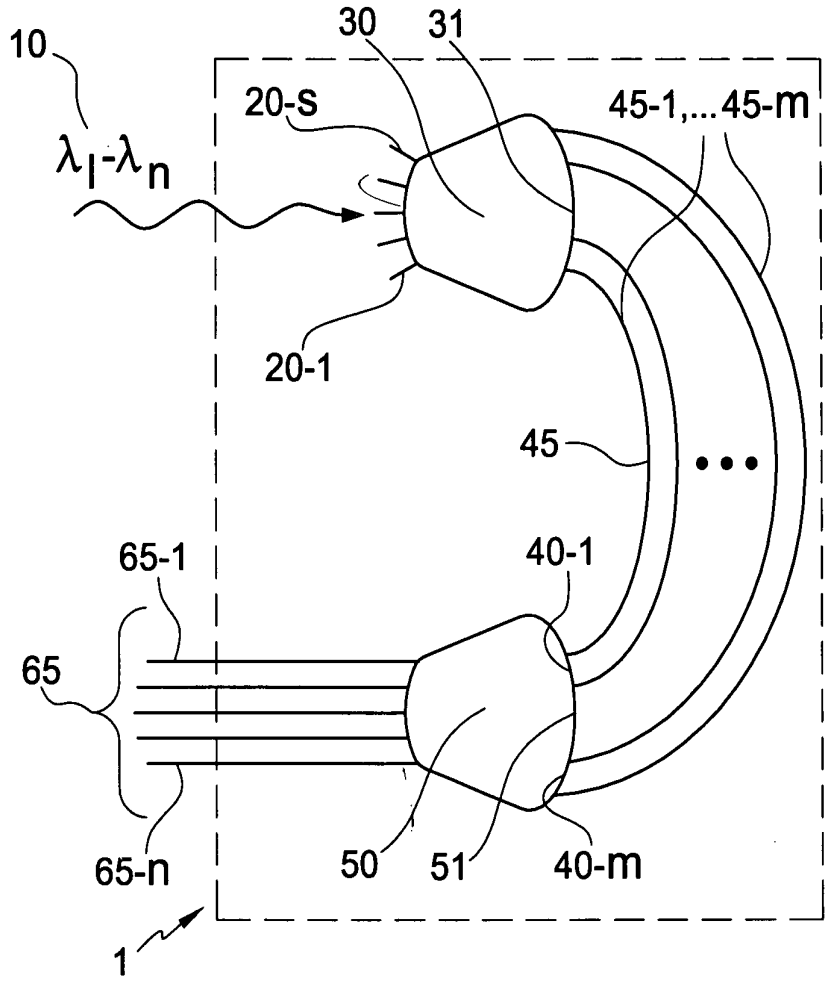

FIG. 1
(PRIOR ART)

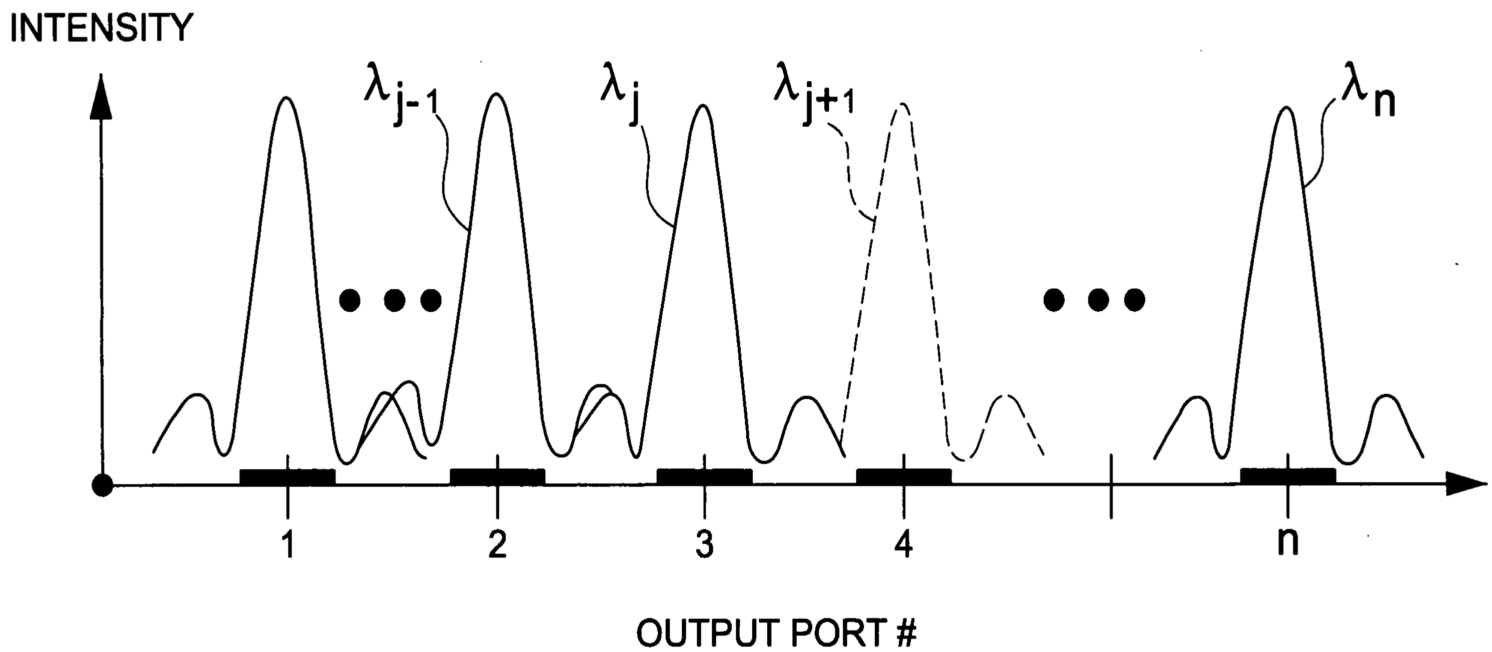

FIG. 2
(PRIOR ART)

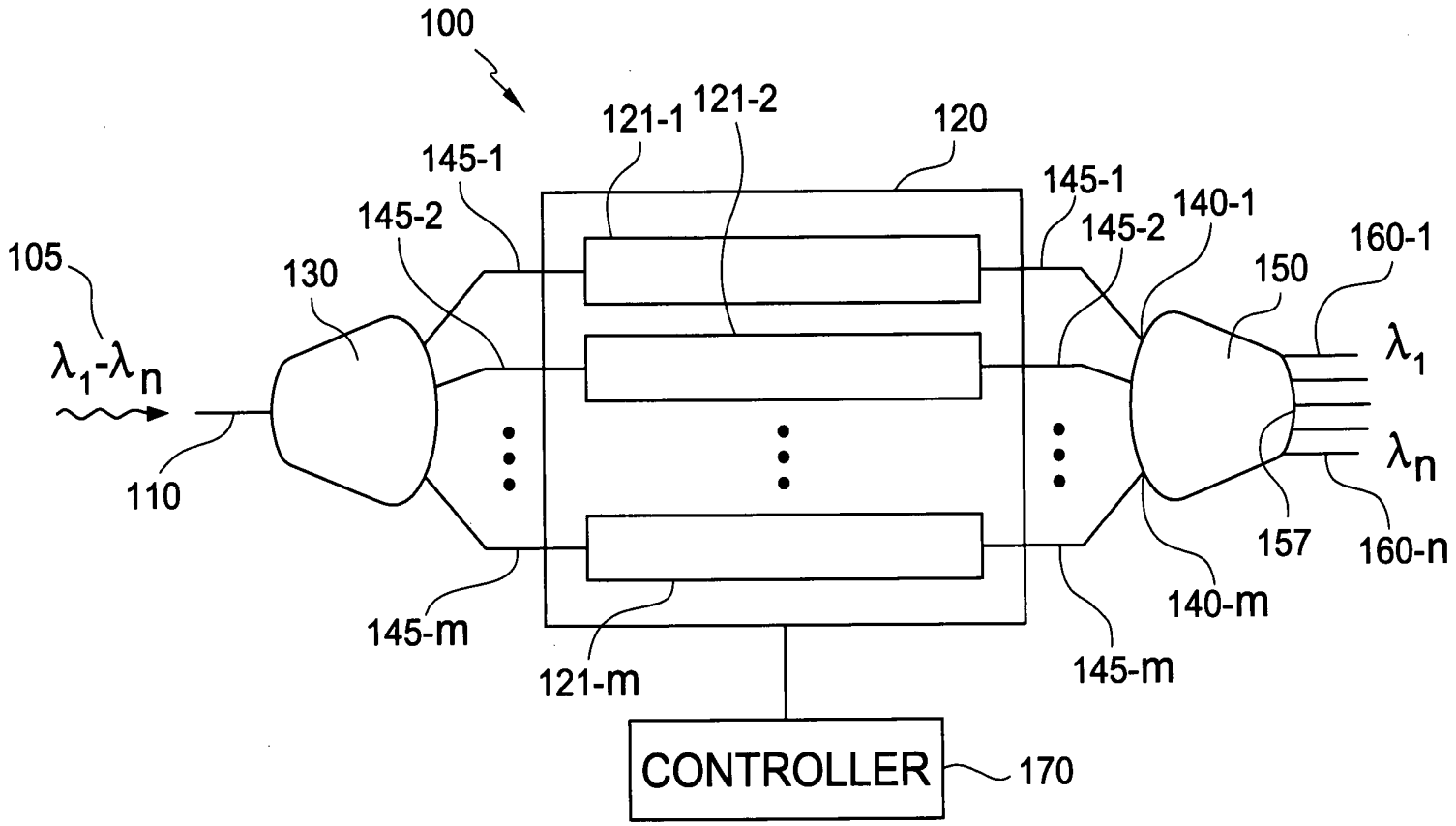

FIG. 3

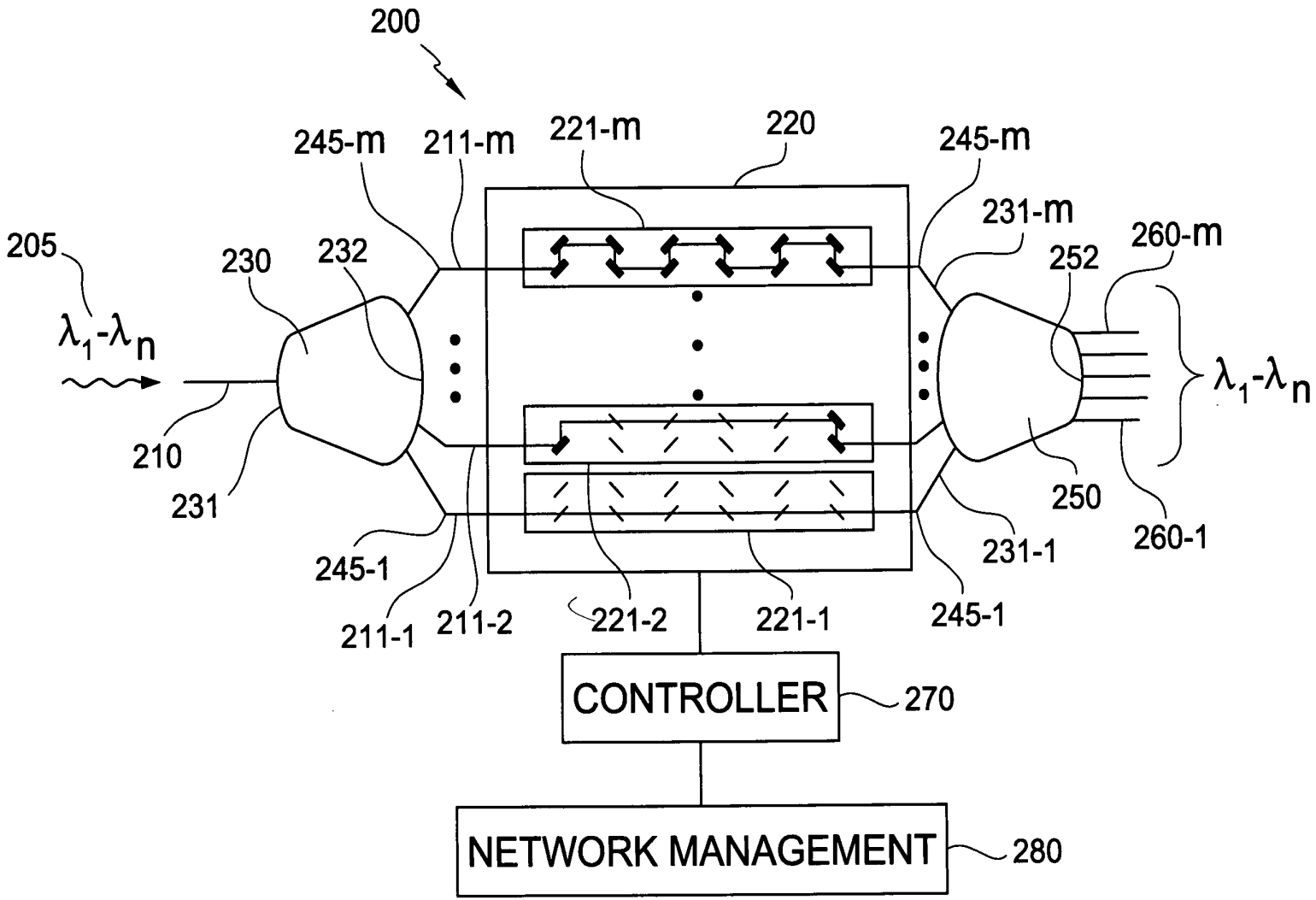
200
245-m
211-m
221-m
220
245-m
231-m
260-m
252
205
230
232
λ_1-λ_n
λ_1-λ_n
210
231
250
260-1
231-1
245-1
245-1
211-1
211-2
221-2
221-1
CONTROLLER
270
NETWORK MANAGEMENT
280

FIG. 4

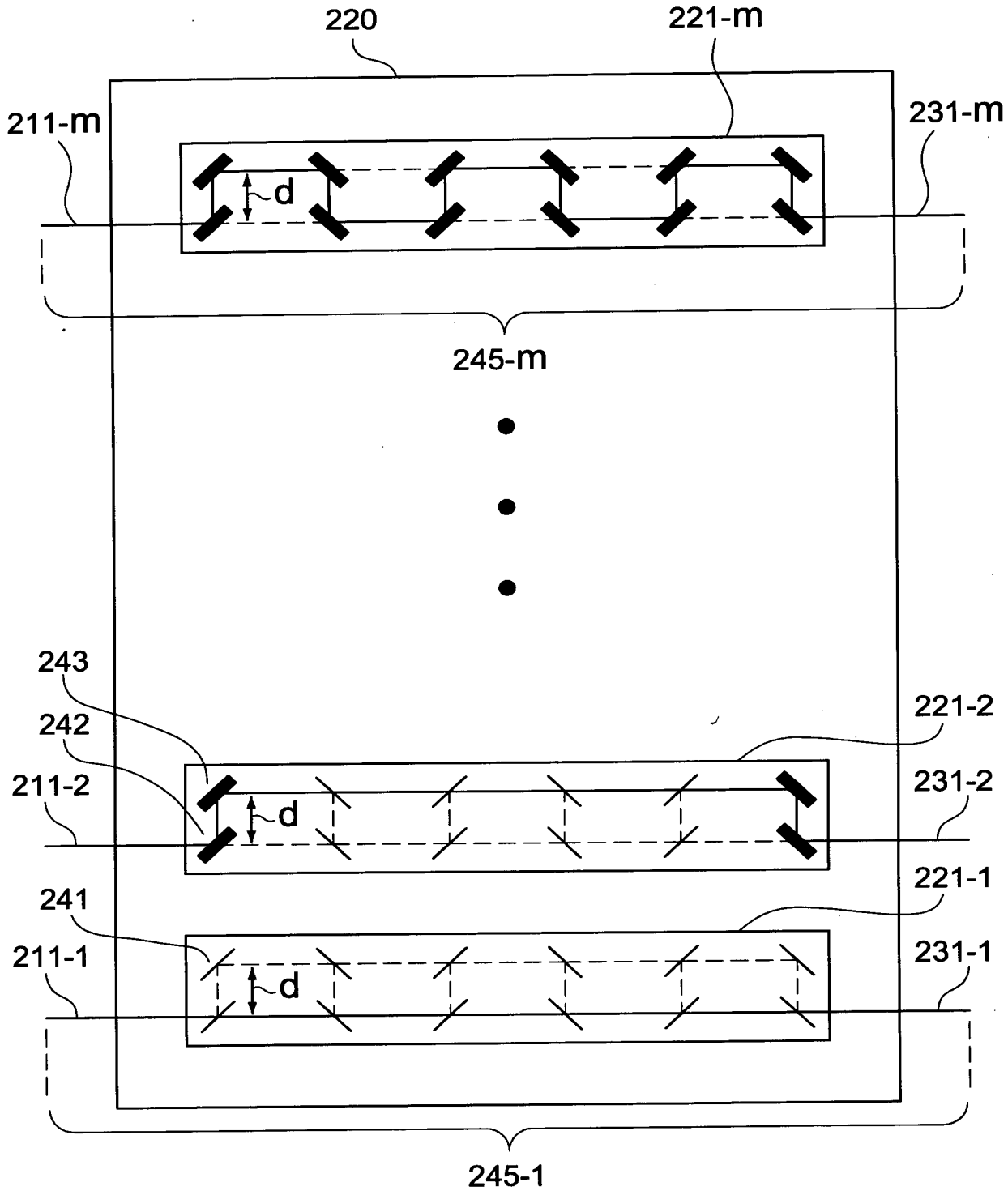
220
221-m
211-m
231-m
d
245-m
243
242
221-2
211-2
231-2
d
241
221-1
211-1
231-1
d
245-1

FIG. 5

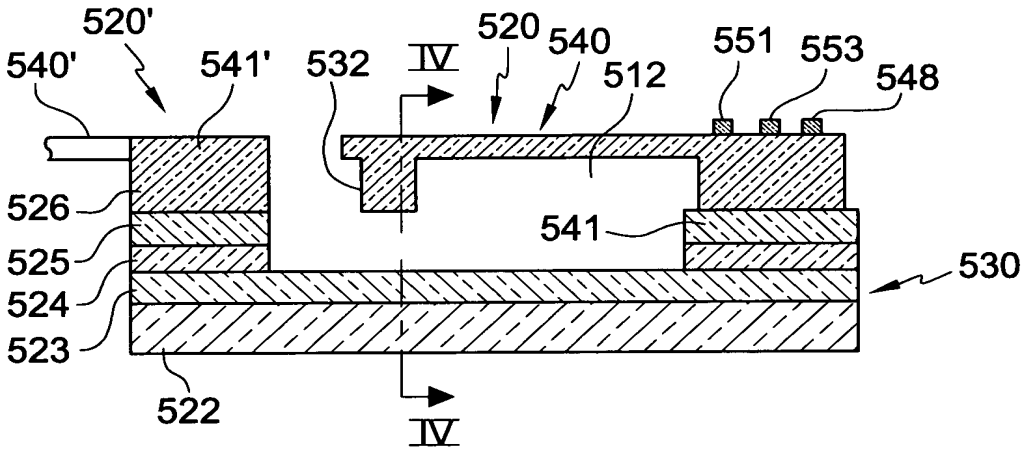
520'
540'
541'
532
IV
520
540
512
551
553
548
526
541
525
530
524
523
522
IV

FIG. 6

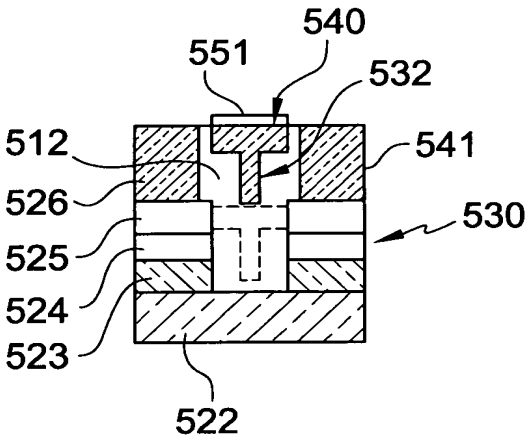
540
551
532
512
541
526
530
525
524
523
522

FIG. 7

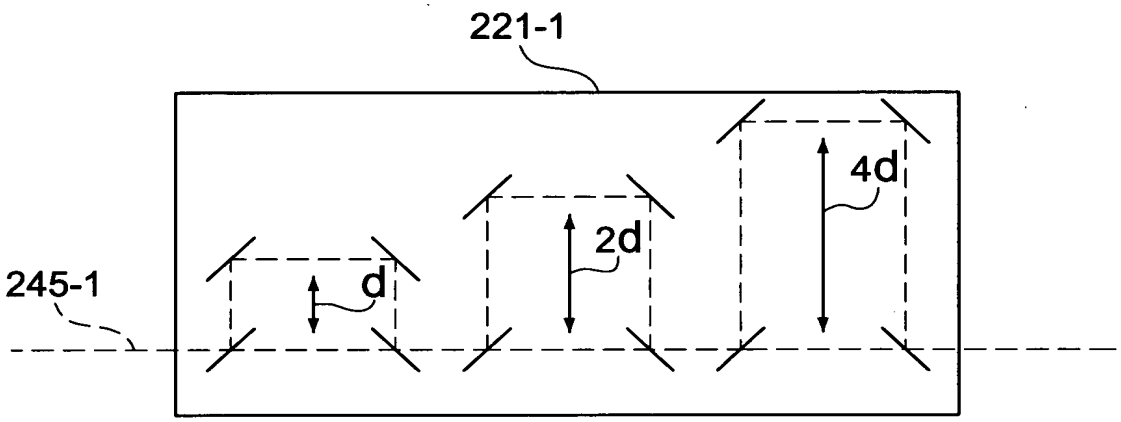

FIG. 8

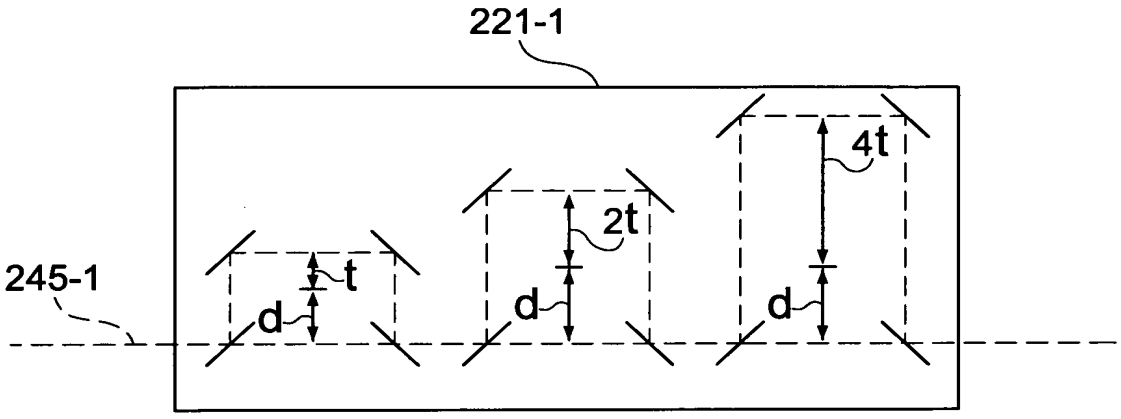

FIG. 9

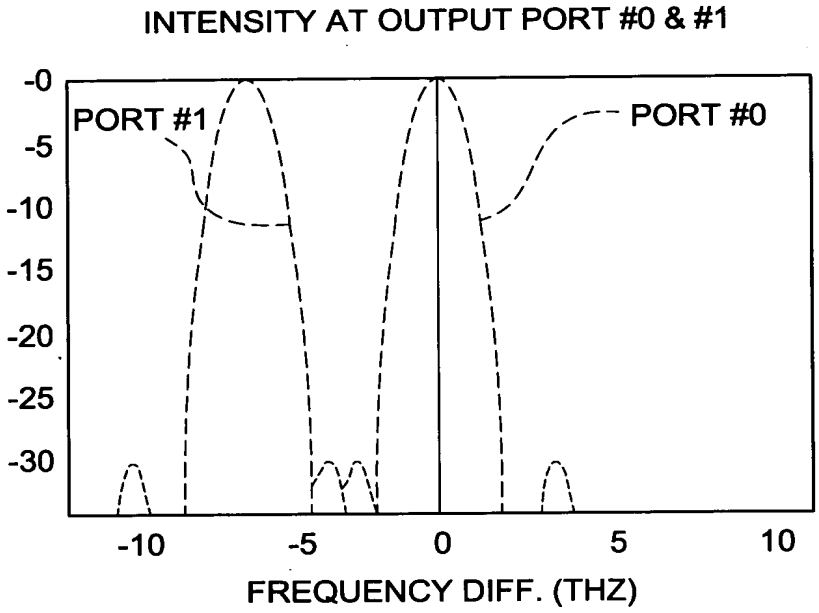

FIG. 10A

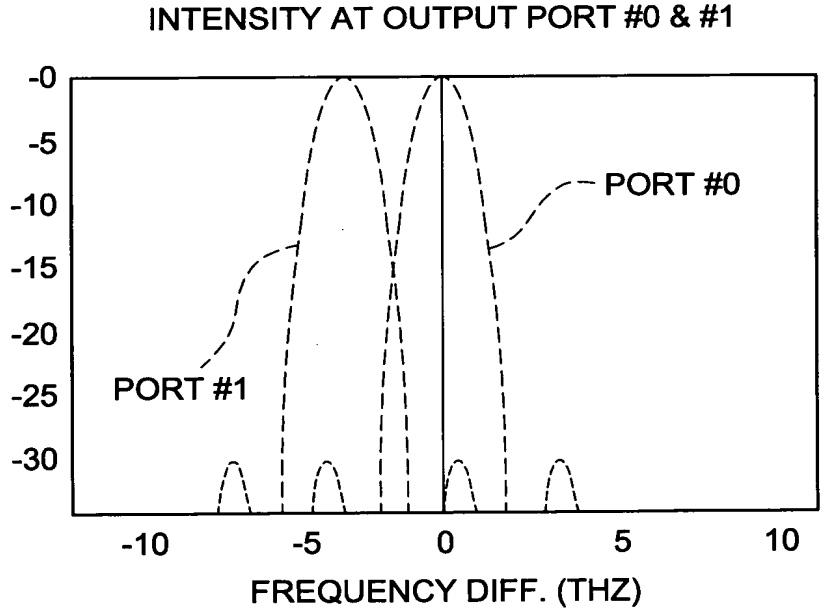

FIG. 10B

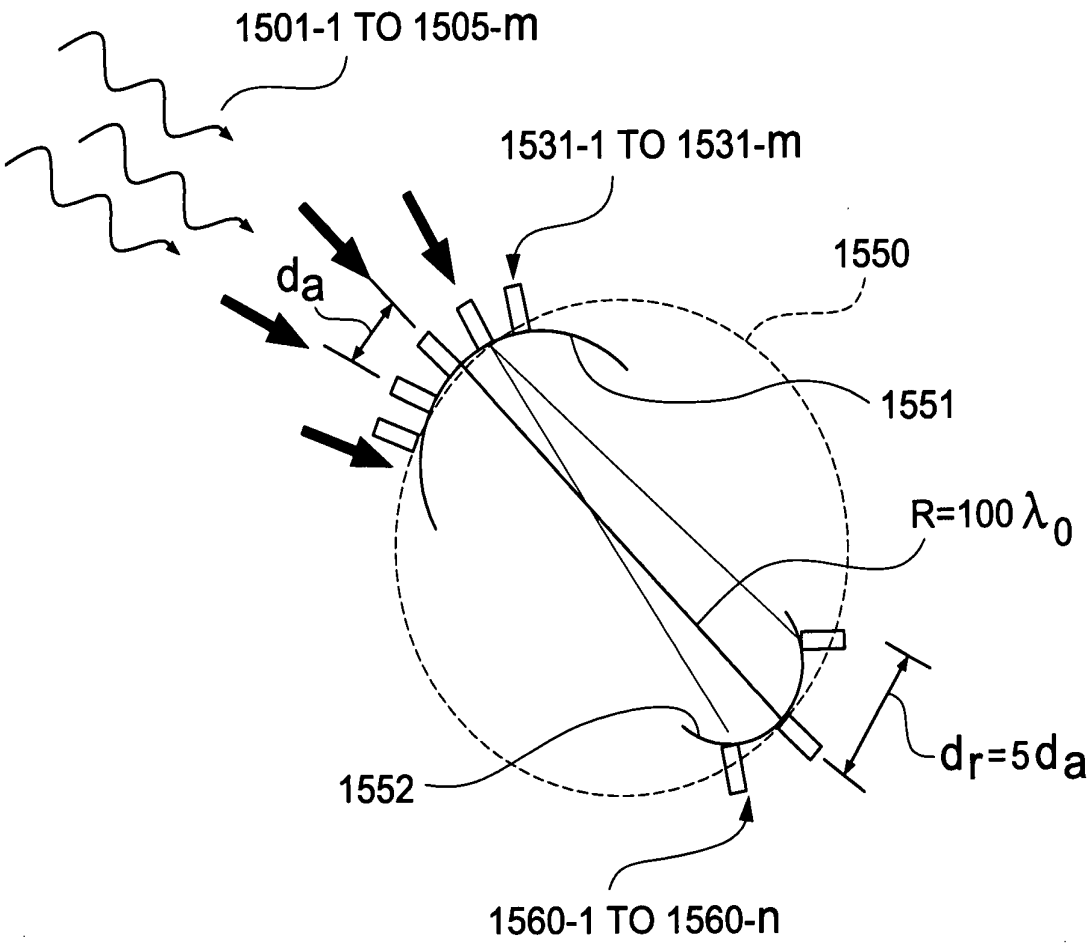
1501-1 TO 1505-m
1531-1 TO 1531-m
1550
d_a
1551
R=100 λ_0
d_r=5d_a
1552
1560-1 TO 1560-n

FIG. 11

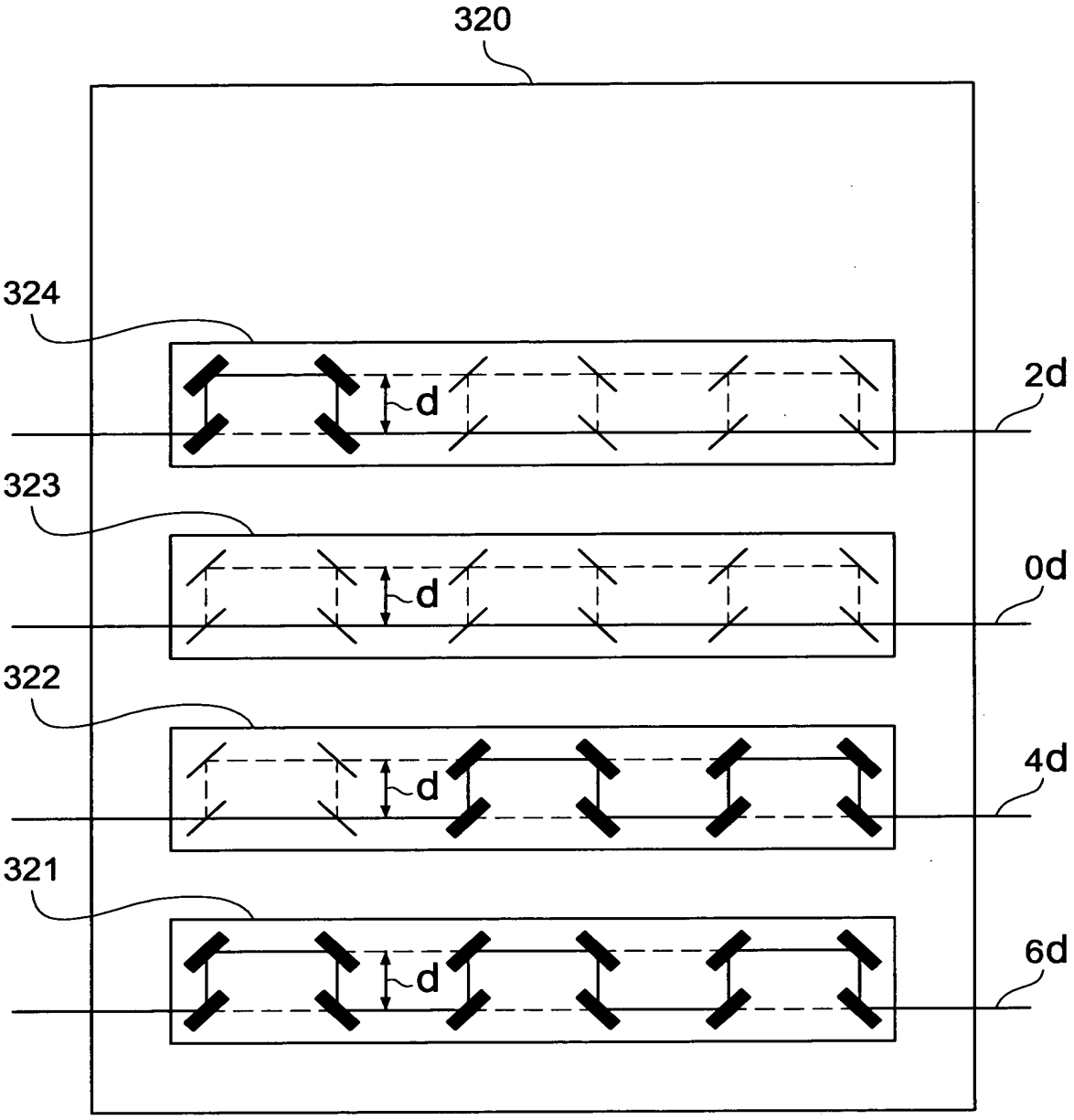

FIG. 12

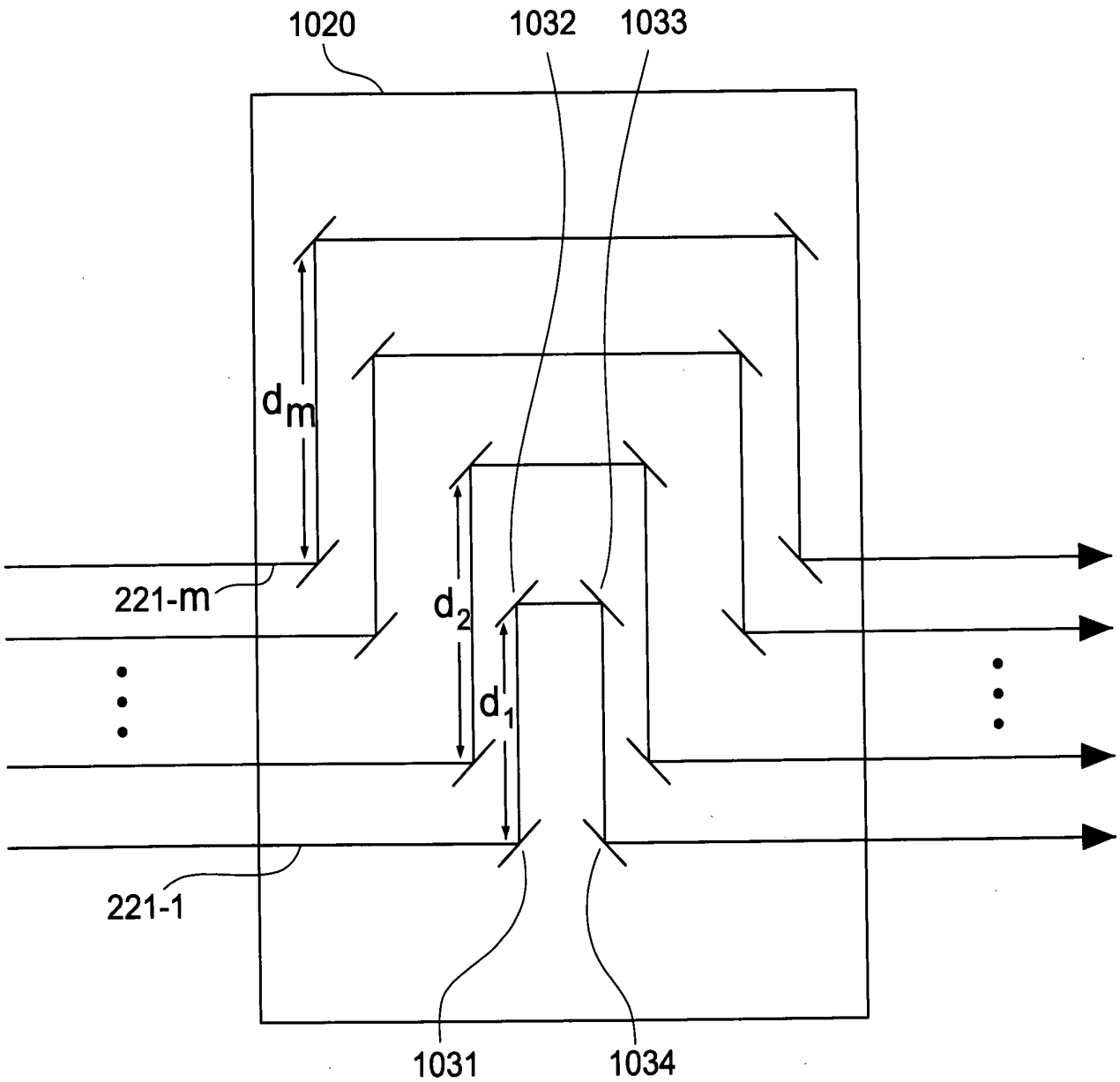

FIG. 13

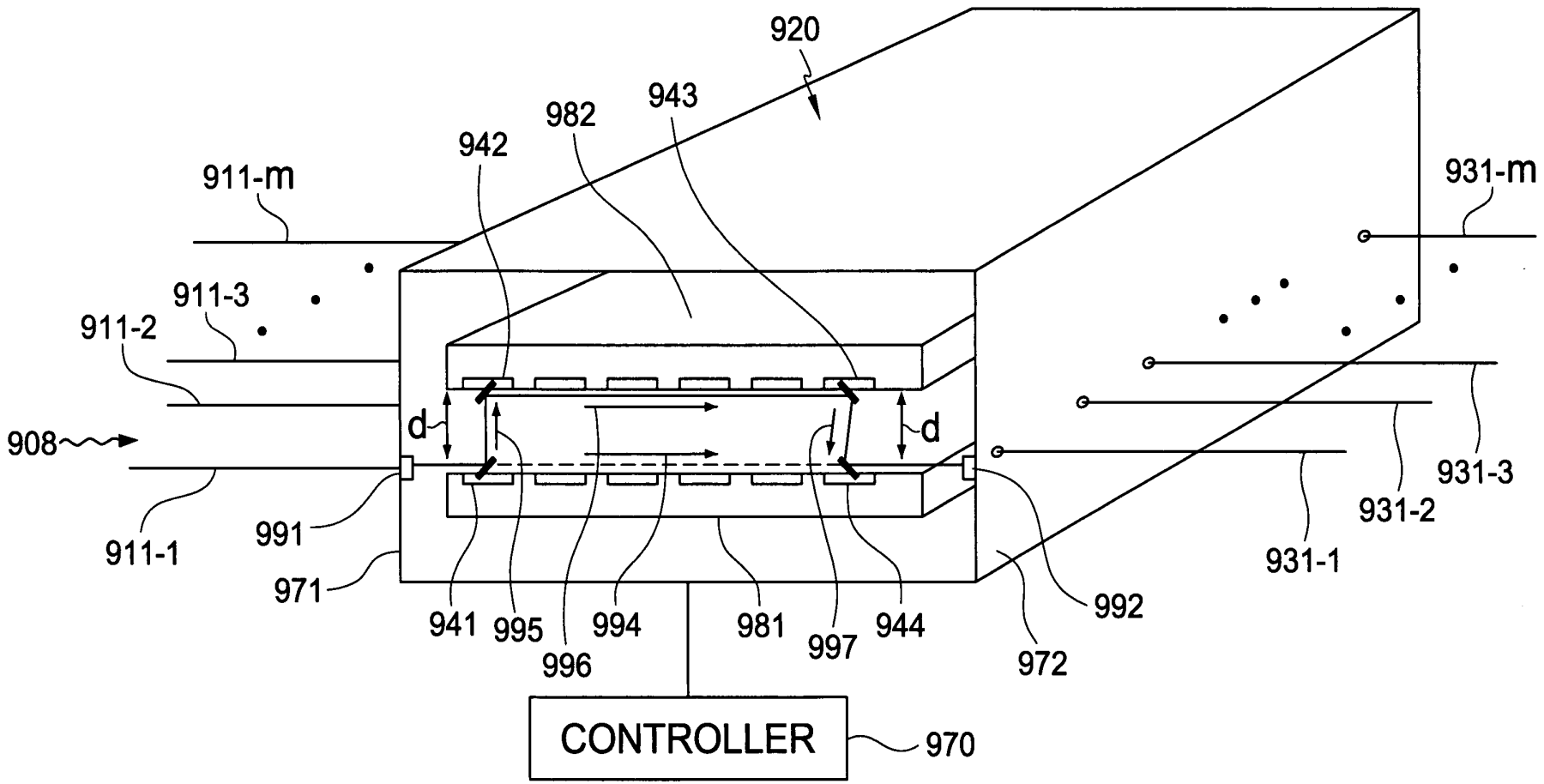
920
943
982
942
911-m
931-m
911-3
911-2
908
d
d
911-1
991
971
941
995
996
994
981
997
944
972
992
931-1
931-2
931-3
CONTROLLER
970

FIG. 14

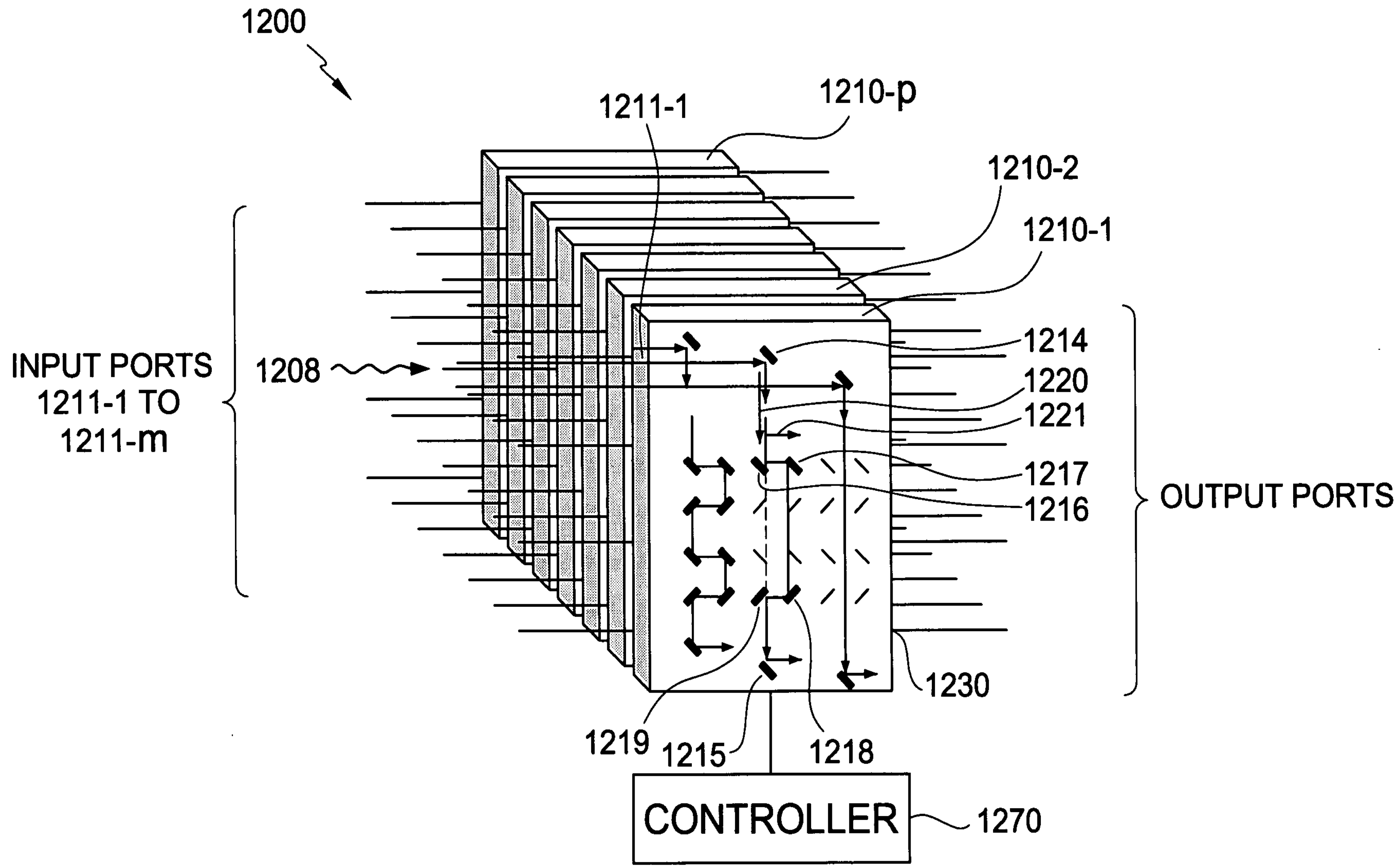
1200
1211-1
1210-p
1210-2
1210-1
INPUT PORTS
1211-1 TO
1211-m
1208
1214
1220
1221
1217
1216
OUTPUT PORTS
1230
1219
1215
1218
CONTROLLER
1270

FIG.15

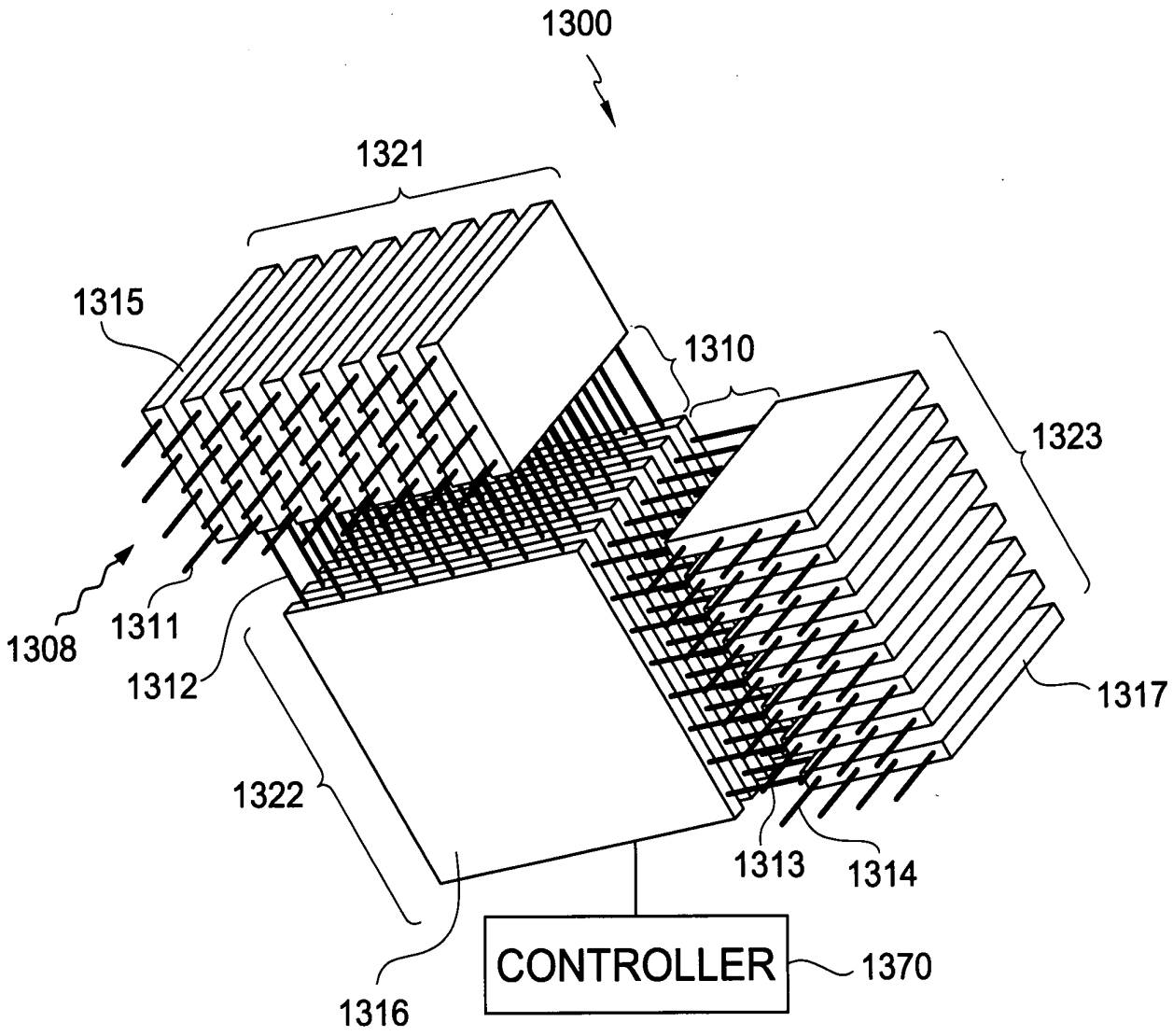

FIG. 16

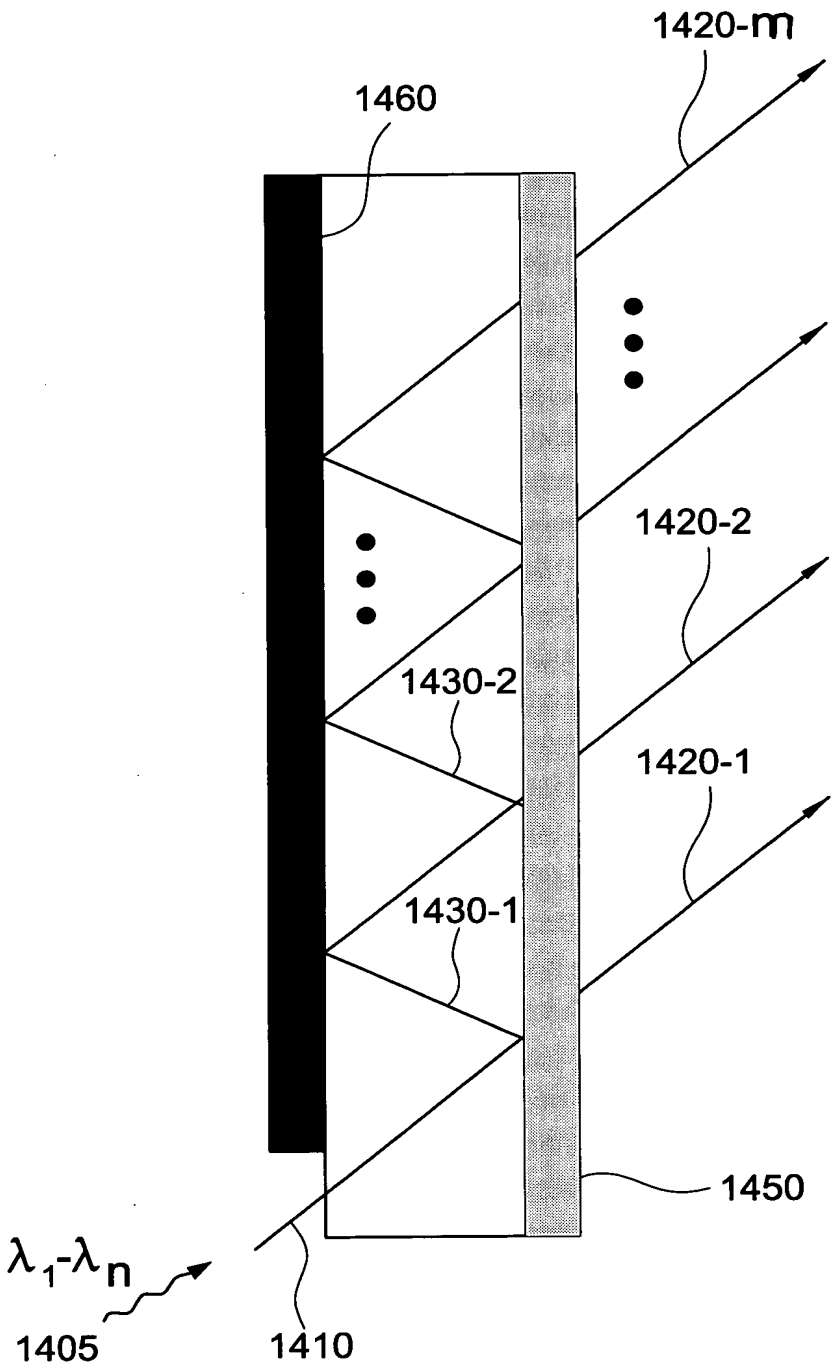

FIG. 17

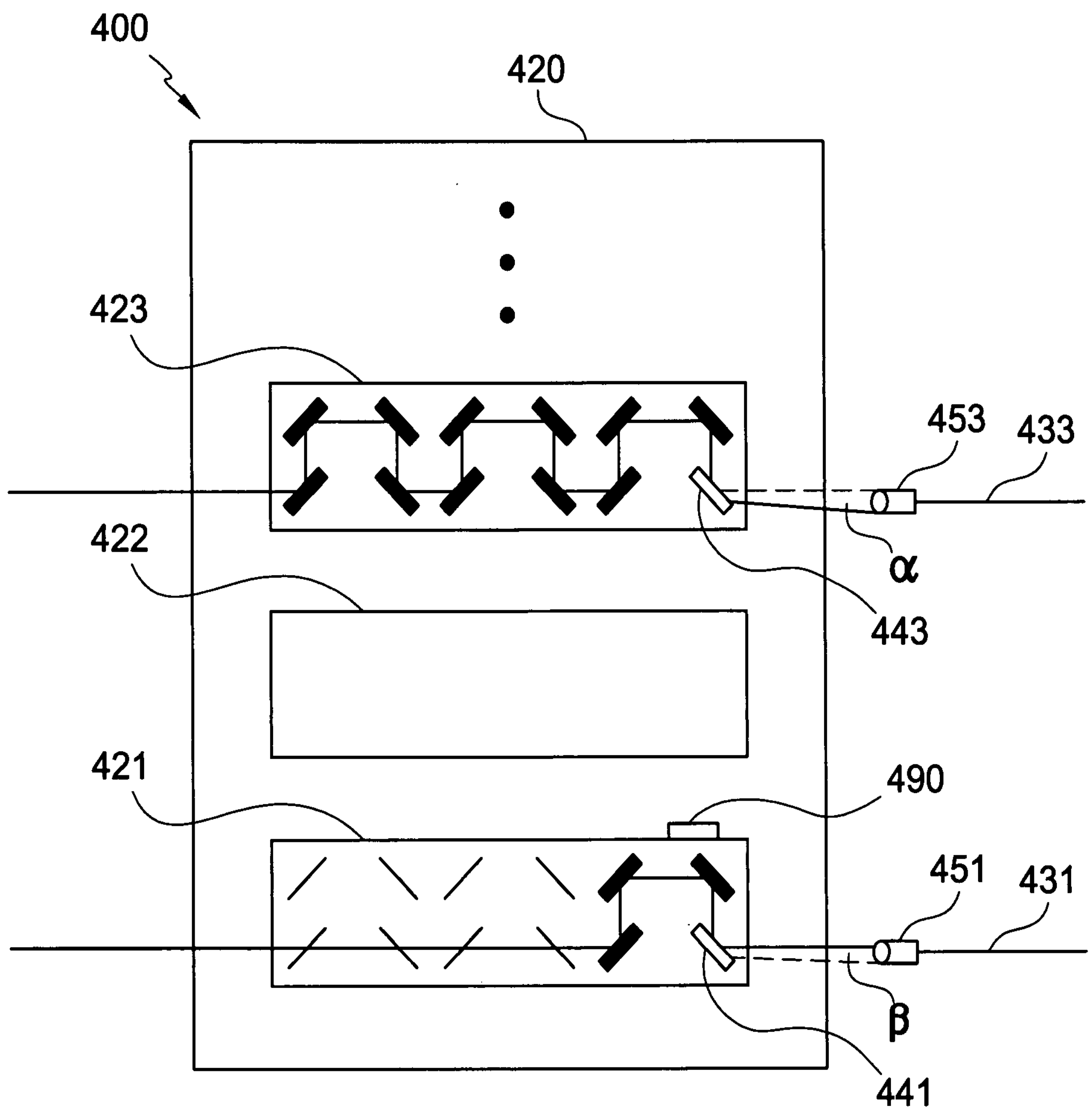

FIG. 18